

In the application of:

Takashi NOMA et al.

Serial No.:

10/828,556

Filing Date:

April 21, 2004

SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD

THEREOF

Examiner: Nitin Parekh

Group Art Unit: 2811

Conf. No.: 5433

PRELIMINARY AMENDMENT

MS RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Prior to the next examination of this application in consequence of the Request for Continued Examination filed concurrently herewith, please amend this application as follows:

The listing of the claims begins on page 2.

The remarks begin on page 5.